

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Mutsumi KIMURA et al.

Application No.: New US Application

Filed: January 27, 2004

Docket No.: 118215

For: THIN FILM TRANSISTOR TYPE DISPLAY DEVICE, METHOD OF
MANUFACTURING THIN FILM ELEMENT, THIN FILM TRANSISTOR CIRCUIT
BOARD, ELECTRO-OPTICAL DEVICE, AND ELECTRONIC APPARATUS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. Relevance of the non-English language references is discussed in the present specification.
- ☒ 3. English-language Abstracts of the non-English language references are attached hereto.
- ☒ 4. A computer-generated English translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office ([<http://www.jpo.go.jp>]), and is attached, but has not been reviewed for accuracy. See References 1-6.

Respectfully submitted,



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Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 118215		APPLICATION NO. New US Application	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANTS Mutsumi KIMURA et al.			
				FILING DATE January 27, 2004			

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS

FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
	1	JP A 6-308872 (with abstract and translation)	11/04/1994	Japan		
	2	JP A 2003-297974 (with abstract and translation)	10/17/2003	Japan		
	3	JP A 2003-142666 (with abstract and translation)	05/16/2003	Japan		
	4	JP A 2003-298006 (with abstract and translation)	10/17/2003	Japan		
	5	JP A 2001-282423 (with abstract and translation)	10/12/2001	Japan		
	6	JP A 2001-282424 (with abstract and translation)	10/12/2001	Japan		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
	7	"A breakthrough technology...for leading edge microelectronics manufacturing"., www.holtronic.ch,

EXAMINER	DATE CONSIDERED
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Date: January 27, 2004